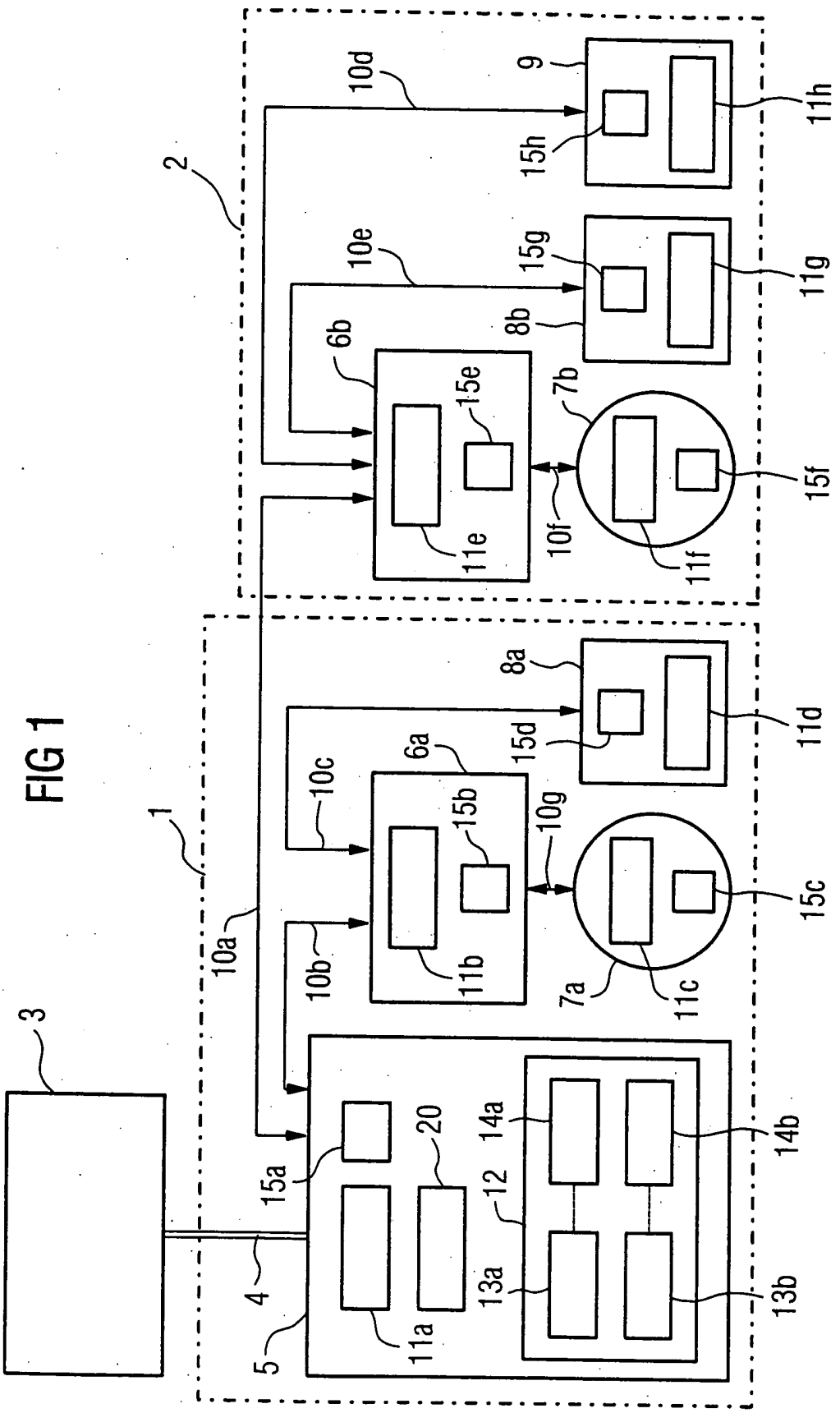
FIG 1


1
2
3
4
5
6a
6b
7a
7b
8a
8b
9
10a
10b
10c
10d
10e
10f
10g
11a
11b
11c
11d
11e
11f
11g
11h
12
13a
13b
14a
14b
15a
15b
15c
15d
15e
15f
15g
15h
20

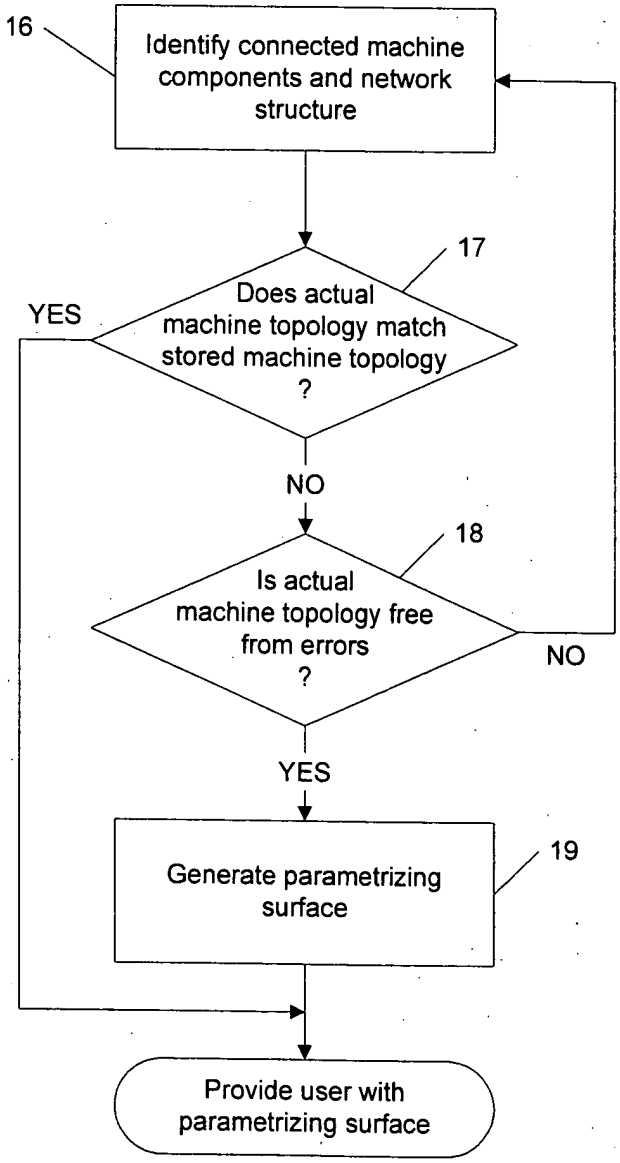

16
Identify connected machine components and network structure
17
Does actual machine topology match stored machine topology ?
YES
NO
18
Is actual machine topology free from errors ?
NO
YES
Generate parametrizing surface
19
Provide user with parametrizing surface

FIG. 2